

# EDR Step 2 Way Forward

16 December 2025, SG-EDR-45



# EDR „STEP 2“ WAY FORWARD

## Motivation

- EDR Step 2 shall improve the existing UN-R160 by meaningful extensions
- Data elements discussed but not considered in Step 1 were taken under review
- Keep focus on crash analysis rather than doing in-field monitoring of functions
- During IWG-29 (October 1<sup>st</sup>), 43 data elements were in discussion
  - 21 are already agreed between the participants
  - 17 are open and new compared to Step 1
  - 5 are open and part of Step 1 but subject to memory increase
- CLEPA ran an analysis of memory consumption



# EDR „STEP 2“ DATA ELEMENTS

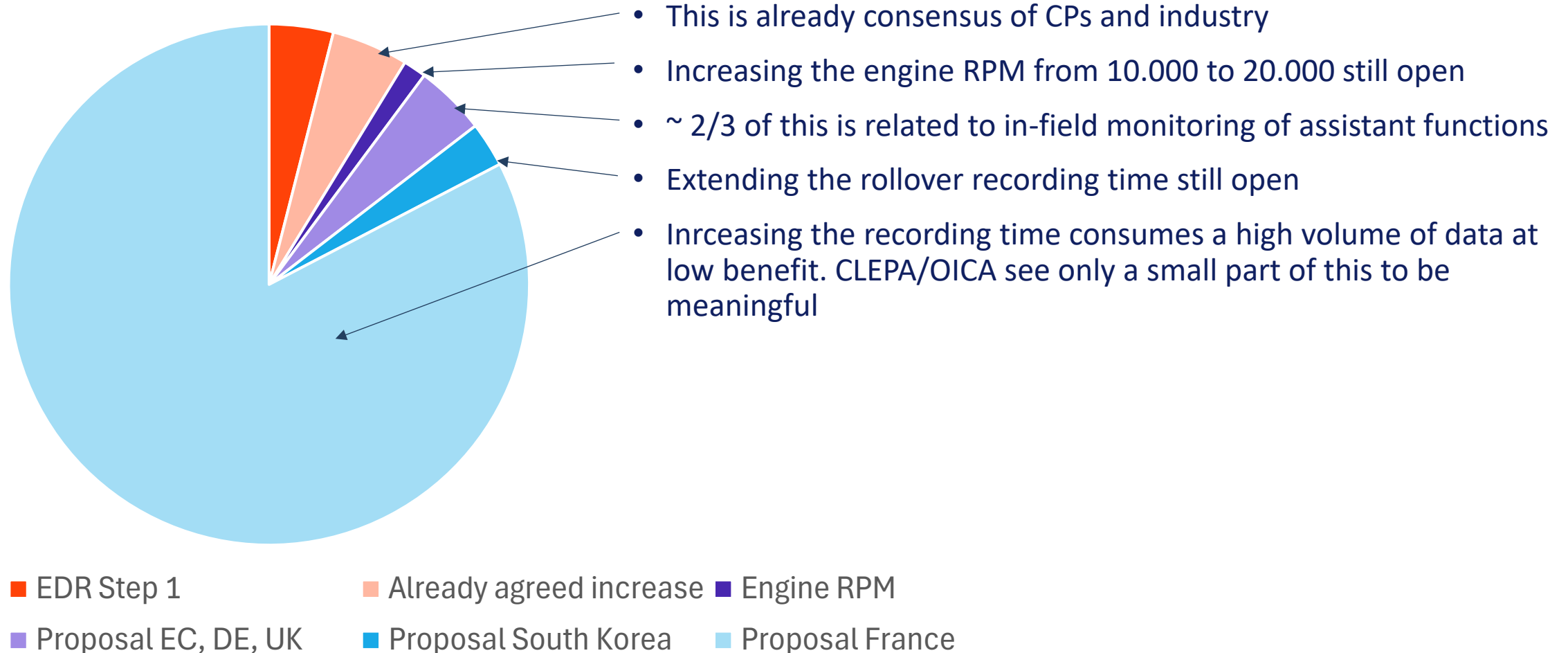
## Overview of Proposals

<b>EDR-DSSAD-IWG-29-07 working document Step 2 R160</b>	21 data elements
<b>Proposal of EC, DE, UK (Document EDR-DSSAD-IWG-29-13)</b>	15 data elements
<b>Proposal of South Korea for Rollover</b>	1 data element
<b>Proposal of France: all data elements for 10 Hz and 30 sec duration</b>	4 data elements
<b>Data Elements CLEPA / OICA requested but not considered</b>	2 data elements



# EDR „STEP 2“ DATA ELEMENTS

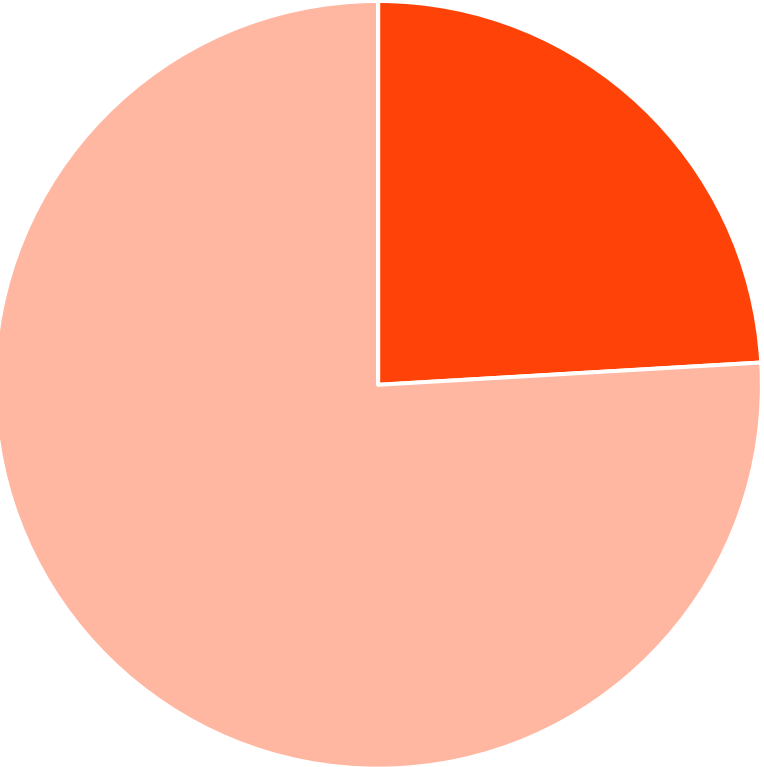
## Analysis of Memory Consumption





# EDR „STEP 2“ DATA ELEMENTS

Proposal



■ EDR Step 1   ■ OICA/CLEPA proposal

- CLEPA and OICA will consolidate the proposals into one new version of the working document
- Considering Engine RPM and Rollover proposals as well as parts of the proposals of EC, DE, UK and France
- Review the proposal document in IWG (February 2<sup>nd</sup> - 4<sup>th</sup>) before sending it to GRSG for April 2026 session
  
- Buffer and memory increase by factor 3 is a big burden
- Due to higher complexity and missing cost-benefit-analysis, the justification of the CPs is not sufficient
- Rear AEBS shall be stricken through
- DCAS, ADDW data elements remain in square brackets but are not recommended by industry



# Thank you!

## **CLEPA**

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